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Semiconductor Manufacturing (Thin Films) By Ronald A.  
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## **Patent us7262130 - methods for making**

Accordingly, the inventors devised one or more exemplary methods for making integrated-circuit wiring from materials, such as copper-, silver-, and gold-based metals.

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The term physical vapor deposition originally appeared in the 1966 book Vapor Deposition by C. F. Sputter deposition: including semiconductor devices,

## **Spts - metal deposition equipment for**

PVD; Semiconductor. LPCVD technologies covering conventional PVD through to MOCVD . Metal Deposition Technologies. Standard PVD conventional sputter modules

## **Patent us7262505 - selective electroless-plated**

Structures and methods are provided which include a selective electroless copper metallization. The present invention includes a novel methodology for forming copper

## **Patent us5969422 - plated copper interconnect**

A high conductivity interconnect structure is formed by electroplating or electroless plating of Cu or a Cu-base alloy on a seed layer comprising an alloy of a

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### **Patent us7763545 - semiconductor device**

In a semiconductor device manufacturing method etching of a conductive film that acts as an electrode of the capacitor formed over a semiconductor

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Accordingly, the inventors devised one or more exemplary methods for making integrated-circuit wiring from materials, such as copper-, silver-, and gold-based metals.

**Chapter 9 pvd materials and processes**

that are utilized in physical vapor deposition Sputter Deposition Applied to Semiconductor Manufacturing. Edited By Ronald A. Powell and Stephen M. Rossnagel.

**Patent us5628659 - method of making a field**

A system and method is available for fabricating a field emitter device, where in an emitter material, such as copper, is deposited over a resistive layer which has

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**Patent us7067421 - multilevel copper interconnect**

Structures and methods provide multilevel wiring interconnects in an integrated circuit assembly which alleviate problems associated with integrated circuit size and